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5670 Wilshire Boulevard, Suite 2100
Los Angeles, California 90036-5679
Telephone: (323) 934-2300
Facsimile: (323) 934-0202

DELIVER TO: Examiner Ho, Tu Tu V Art Unit 2818

COMPANY: United States Patent and Trademark Office

FAX NUMBER: (703) 872-9306

FROM: Robert Popa

DATE: November 12, 2004

TOTAL NO. OF PAGES (INCLUDING THIS PAGE): 5

SUBJECT : Response to Restriction Requirement
U.S. Application No. 10/627,406
Andre' DeHon et al.
"Sublithographic Nanoscale Memory Architecture"
Our Ref: B-5714NP 621116-2/AS

REMARKS:

Examiner Ho:

Enclosed is a response to the Office Action mailed on October 15, 2004 consisting of:

Response to restriction requirement - 4 pages

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Andre' DeHon et
al.) Group Art No.: 2818
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Application No: 10/627,406

) Examiner: Ho, Tu Tu V
)

NOV 12 2004

Filed: July 24, 2003

) Re: **RESPONSE TO**
)For: "SUBLITHOGRAPHIC
NANOSCALE MEMORY
ARCHITECTURE") **RESTRICTION REQUIREMENT**
)) Our Ref: B-5174NP 621116-2/AS
)

) Date: November 12, 2004

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is filed in response to the restriction requirement set forth in the Office Action dated October 15, 2004, a reply to which is initially due by November 15, 2004. Please consider the following remarks with respect to the subject application. All remarks herein are made without prejudice.

Remarks begin on page 2 of this paper.

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